

**IN THE SPECIFICATION:**

In the “Related Applications” Section, please replace the paragraphs at Page 1, between lines 12-21 with the following:

**RELATED APPLICATIONS**

C. Visweswariah, “System and Method for Statistical Timing Analysis of Digital Circuits,” Docket Number YOR92003-0403US1, U.S. Patent Application Number (~~to be assigned~~) 10/666,353, U.S. Patent Publication No. 2005/0065765, filed on 09/19/03-09/18/03.

C. Visweswariah, “System and Method for Probabilistic Criticality Prediction of Digital Circuits,” Docket Number YOR92003-0402US1, U.S. Patent Application Number (~~to be assigned~~) 10/666,470, U.S. Patent Publication No. 2005/0066298, filed on 09/19/03-09/18/03.